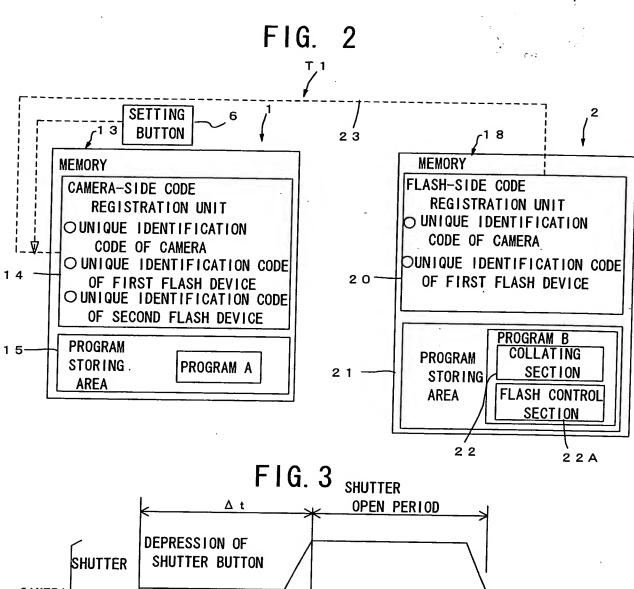
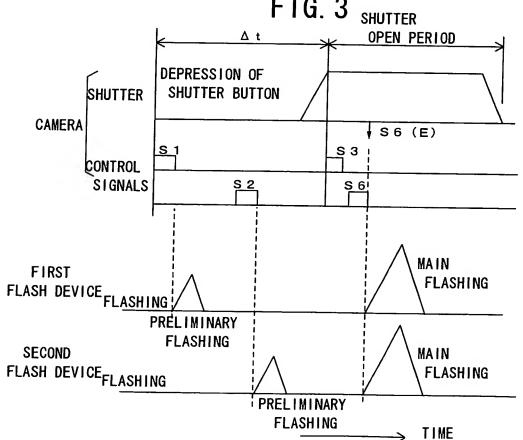
FIG. 1 23 1, 3 RADIO MEMORY FLASH RECEIVING 19 FLASH-SIDE DETERMINING PARŢ CODE CIRCUIT √ 2 O CPU REGISTRATION MEMORY CPU UNIT CAMERA-SIDE **PROGRAM** CODE ìο STORING REGISTRATION **AREA** UNIT 1 2 FLASH BODY UNIT **PROGRAM** RADIO STORING TRANSMITTING AREA **PART** 1 6 1 5 23 REFLECTION RADIO **OBJECT** MEMORY .CE1.
PART
2 0 RECEIVING 19 FLASH-SIDE H -CODE REGISTRATION CPU UNIT **PROGRAM** STORING AREA FLASH BODY UNIT 16





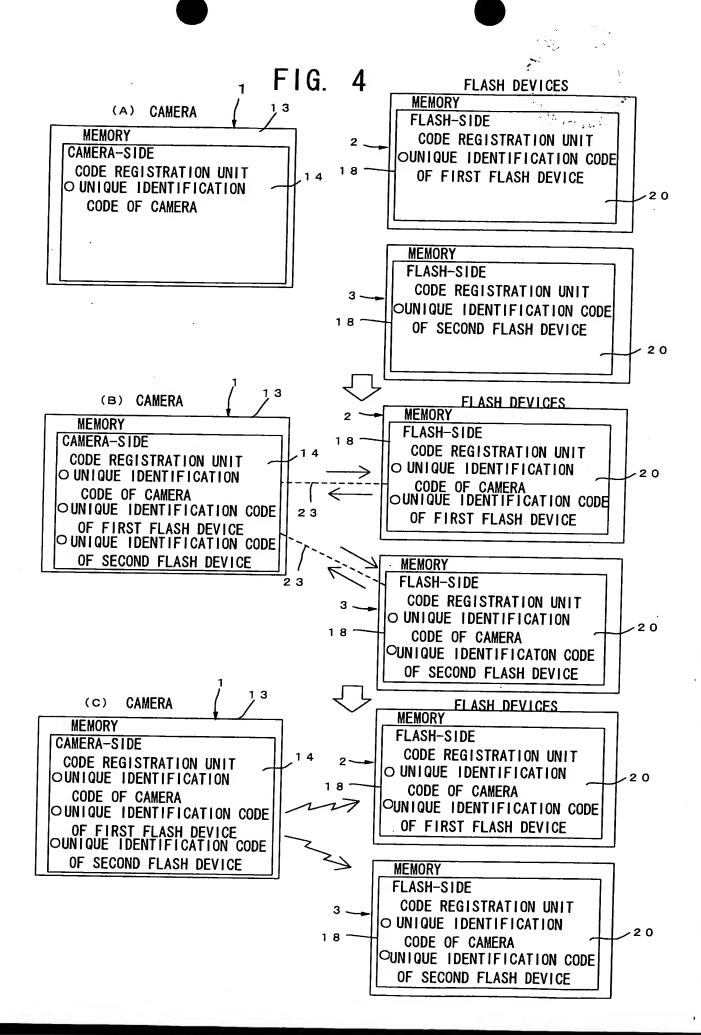


FIG. 5

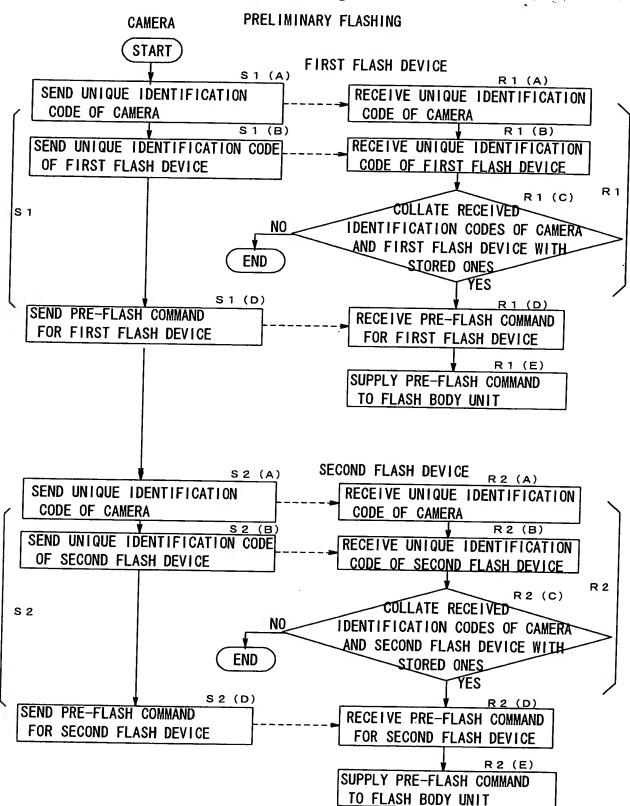
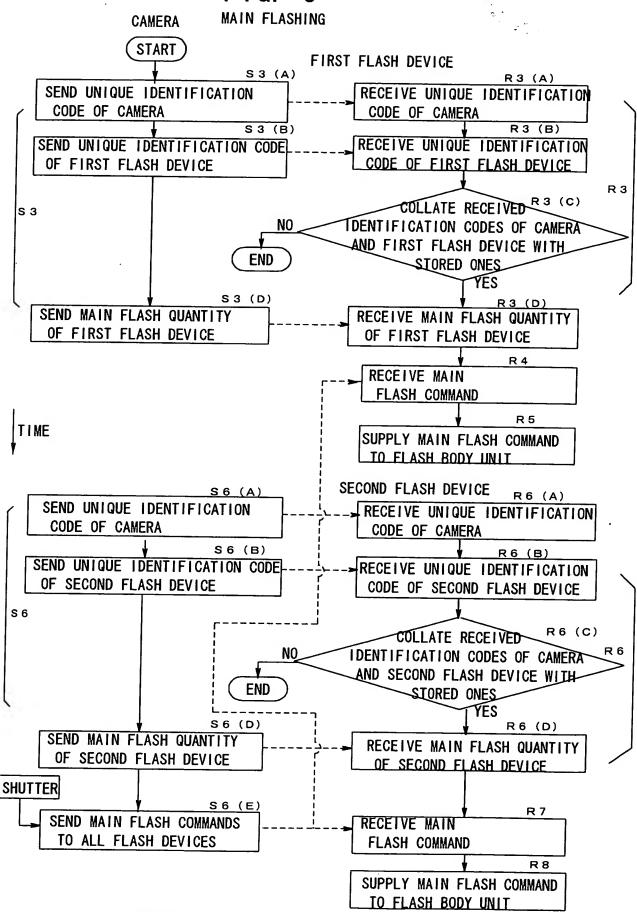
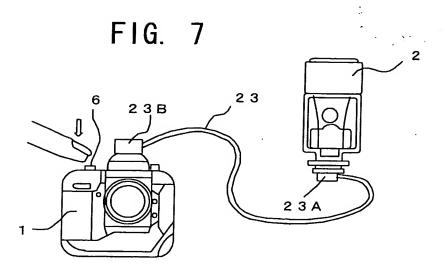
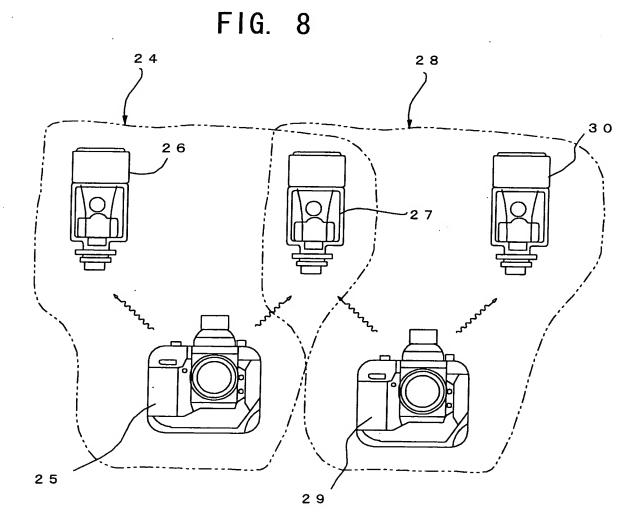
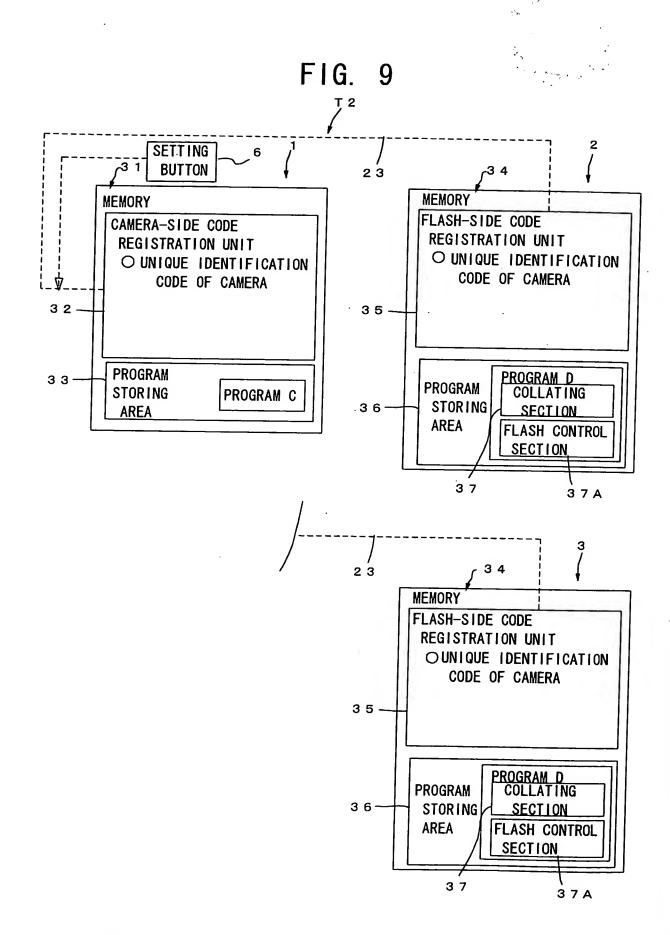


FIG. 6









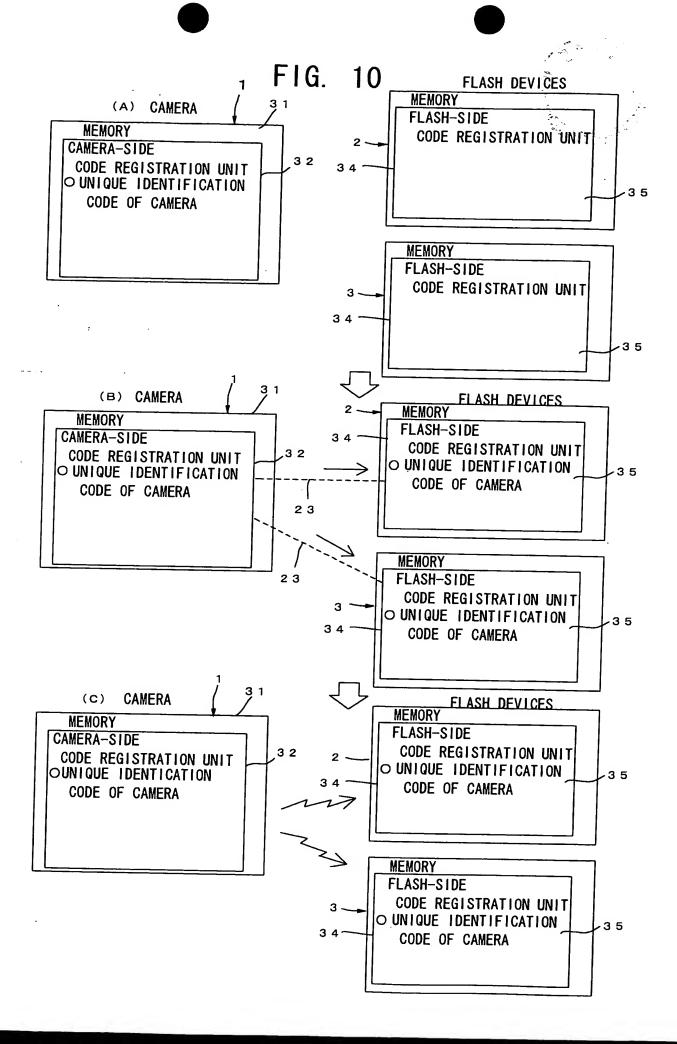


FIG. 11

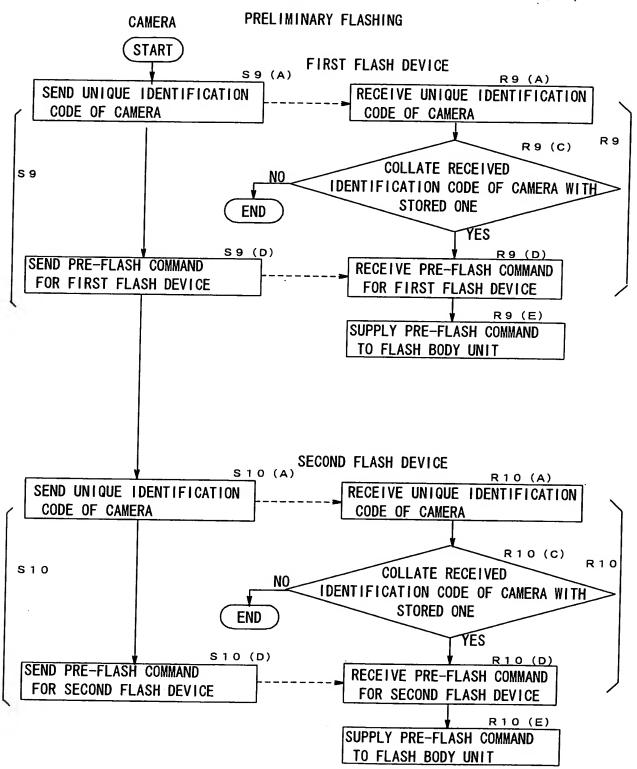


FIG. 12

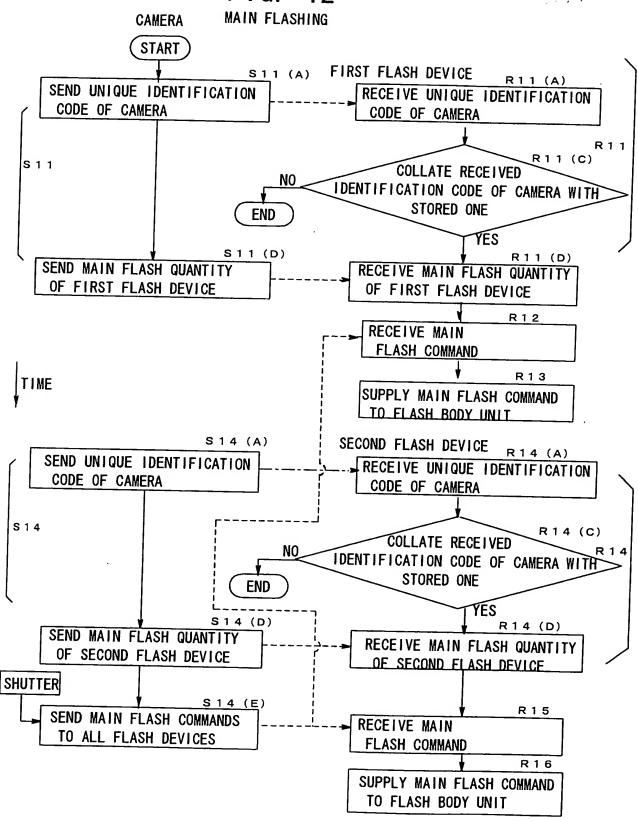
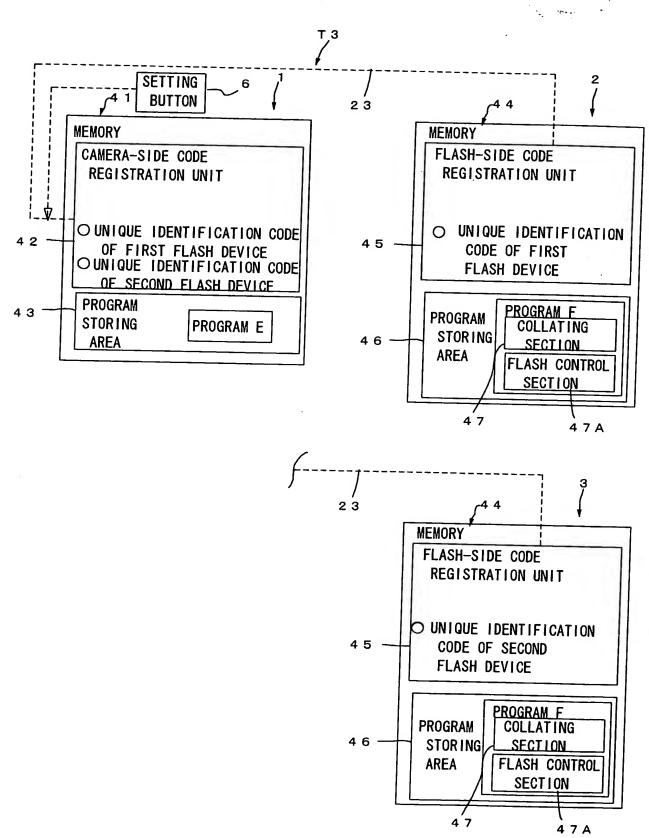


FIG. 13



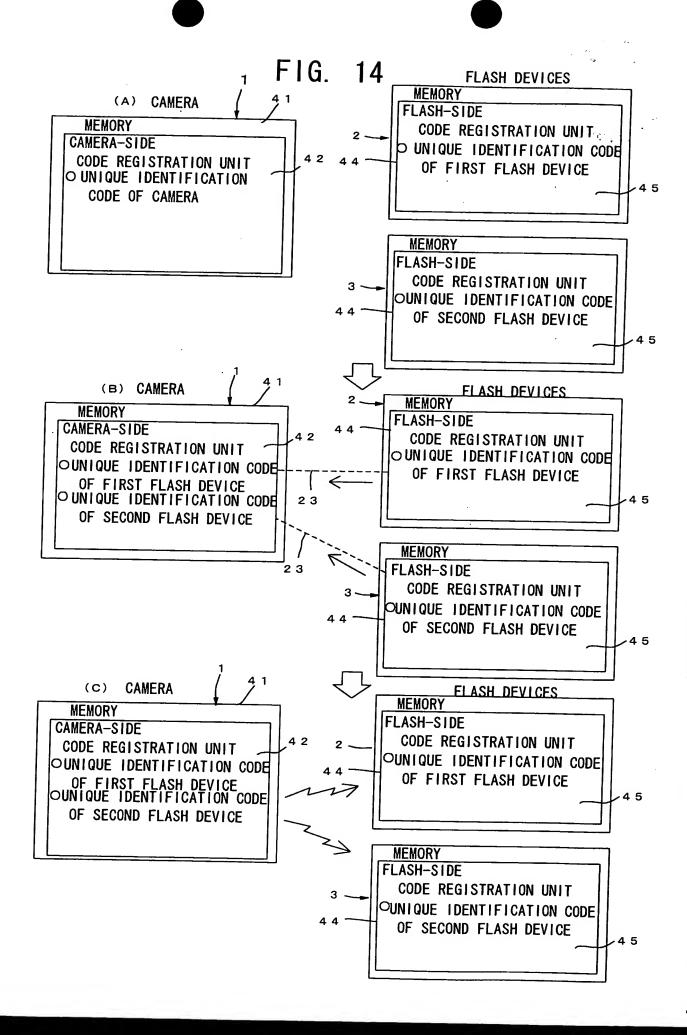


FIG. 15
PRELIMINARY FLASHING

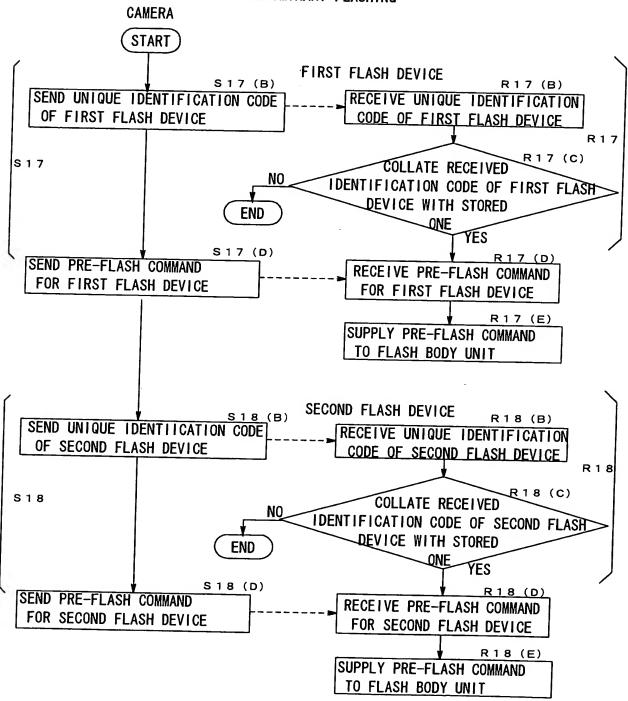


FIG. 16

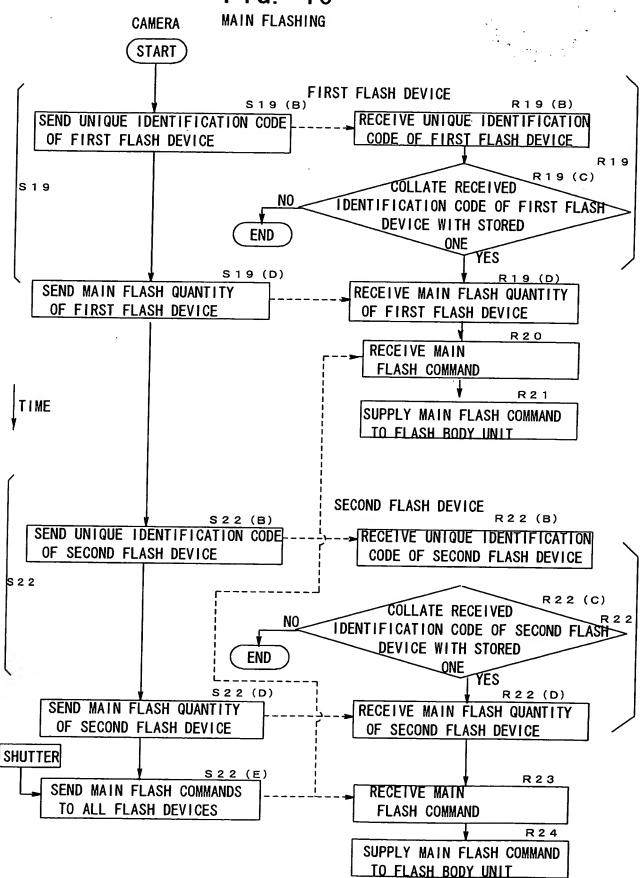


FIG. 17

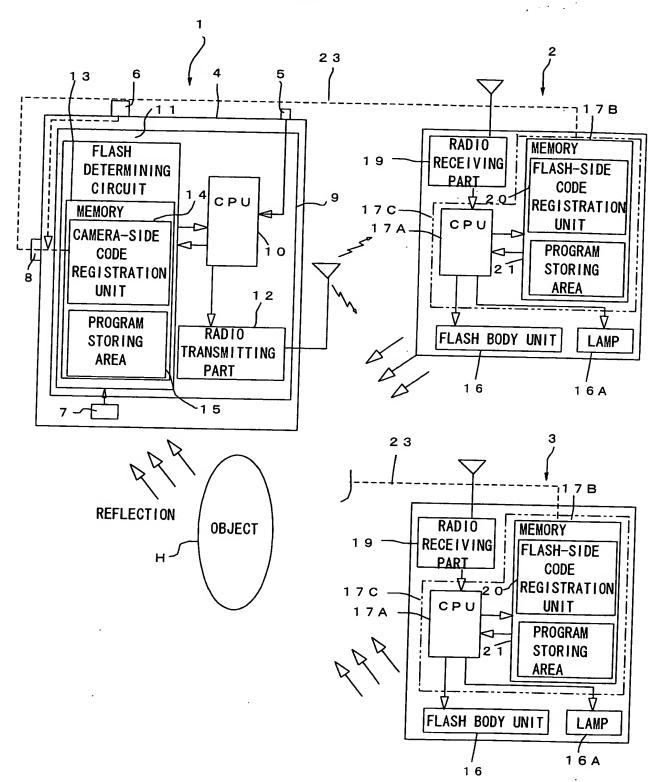


FIG. 18

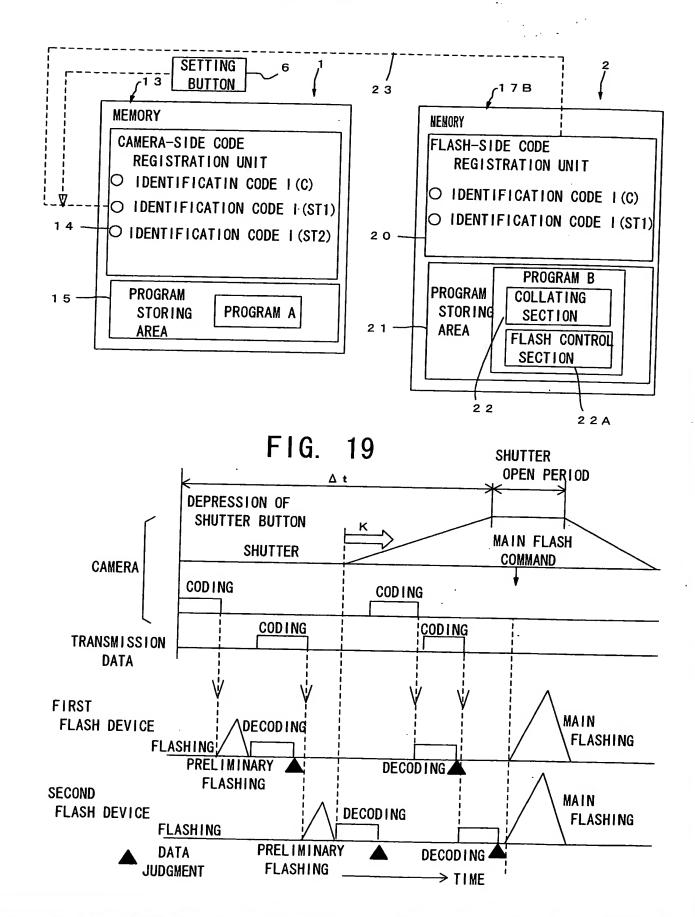
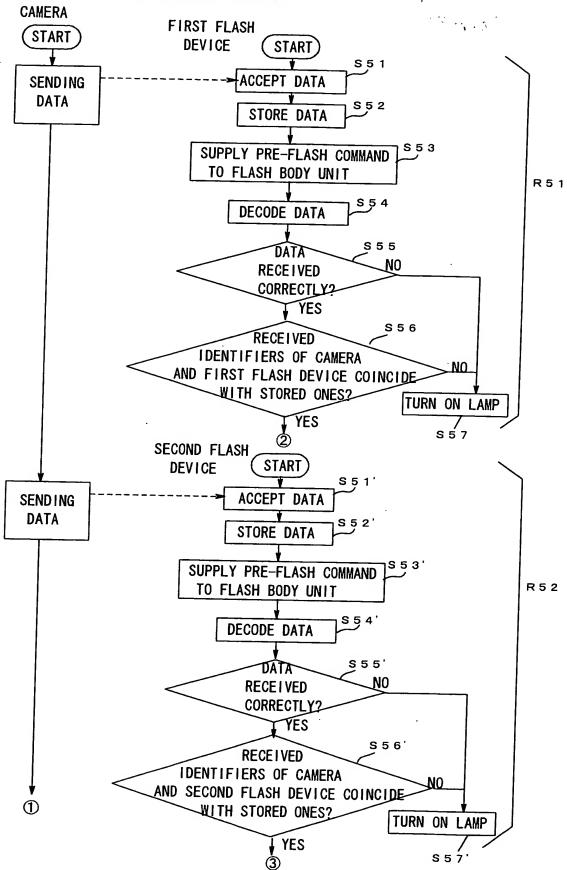


FIG. 20 PRELIMINARY FLASHING



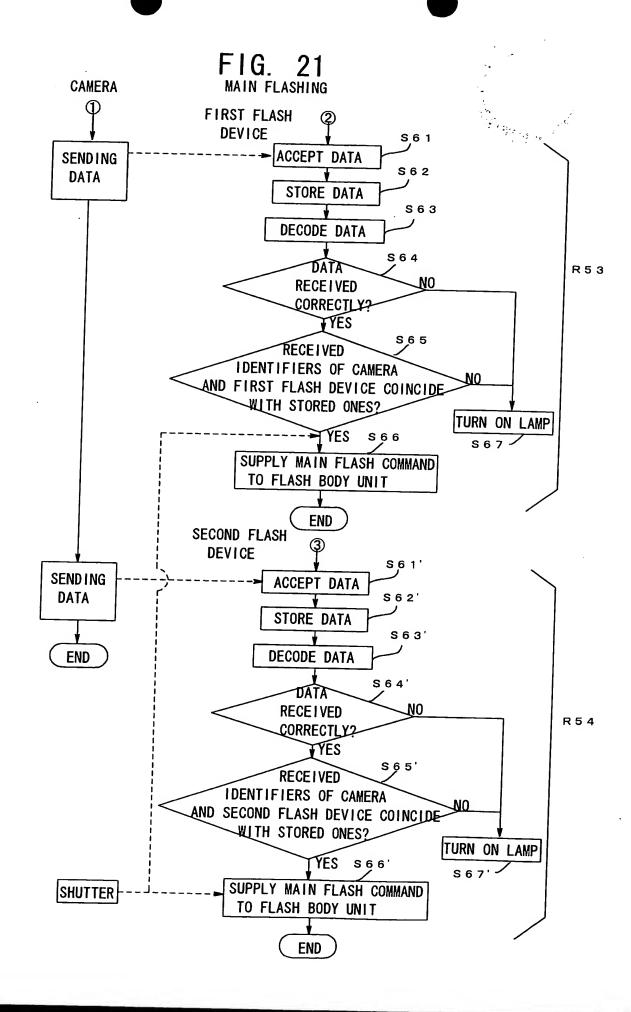


FIG. 22 T 4 5 2 54A RADIO TRANSMITTING RADIO PART RECEIVING 5 6 PART CAMERA FLASH DETERMINING CIRCUIT CAMERA BODY UNIT 5 4 **BODY UNIT** 5 5 PHOTOGRAPHING PHOTOGRAPHING OBJECT 5 3 5 6 **PHOTOGRAPHING** RADIO RECEIVING **PART** CAMERA BODY UNIT

5 5

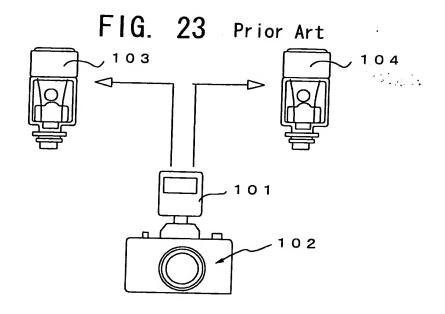


FIG. 24 Prior Art PRELIMINARY FLASHING

